

## METHOD AND APPARATUS FOR SYNCHRONIZATION OVER 802.3AF

The present invention relates to portable wireless devices that are capable of communicating with fixed multiple base stations or Access Points using time division multiple access (TDMA), wherein the Access Points are typically wired together in an Ethernet type protocol IEEE 802.3. More particularly, the present invention relates to synchronizing the timing of the multiple Access Points so as to facilitate the handing off of portable wireless devices from one Access Point to another in the TDMA system. The method described is compatible with the power over the Ethernet scheme described in draft standard IEEE P802.3AF and its successors.

802.3AF is a recent addition to IEEE's enormously popular and successful series of 802.3 Ethernet standards. 802.3AF standardizes the Ethernet interface with regards to data transmission and power delivery using the same cable.

Power delivery via Ethernet must be designed so as not to adversely affect data transmission signals, by adding, for example, noise and/or other types of interference. The Ethernet standard under 802.3AF delivers a "low voltage" of 48VDC to connect Ethernet devices.

The infrastructure for communication typically includes a Basic Service Set (BSS) of an IEEE 802.3 LAN that comprises a server with a plurality of Access Points (AP) that are generally stationary (fixed) and a number of devices (stations or STAs) that can be in wireless communication with at least one AP. The AP connects the stations to the rest of the infrastructure, and permits the stations to communicate with the infrastructure. The stations communicate using a time-division multiple access (TDMA) scheme, which guarantees bandwidth for each connection.

Changing of the location of an STA requires a handing off of its wireless link to one of the neighboring APs. Particularly when the devices are involved in WMTS (Wireless Medical Telemetry Systems) and portable devices that may possibly be attached to a patient to monitor certain vital signs, the disconnection of such devices could have undesirable consequences.

The process of handing-off can be adversely affected by poor timing of the multiple Access Points, meaning that if their timing is not properly synchronized it will be difficult to enable a seamless handover of wireless portable devices from one AP to another.

As shown in Fig. 1, a server 103 is in connection via a local area network 104 using an Ethernet local area networking via Category 5 cables 105,106 with Access Points

107,115. In turn, the Access Points are in wireless contact with a series of mobile stations 105a. Access Point 107 serves area 110, and Access Point 115 serves area 120. Cables 105, 106 carry power and synchronization information as well as data.

5 In the case where one/some of the devices/stations (STAs) 105a move from a first coverage area 110 under AP 107 to a second coverage area 120 under AP 115, the packets of the STAs 105a are handed-off to the newly associated AP 115 and are referred to as 105b when communicating with this other Access Point 115.

10 In the particular example shown in Fig. 1, as the STAs 105a move out of range of their associated AP 107, they will be handed-off from the AP 107 and become associated with 115. Unless suitable measures are taken to ensure that the APs are synchronized, the handed-off STAs can suffer from a service disruption and loss of packets during handoff, as the STAs are forced to stop communication immediately and look for other APs with which to associate.

15 The portable STAs periodically perform a site survey, which provides an overall picture of the neighborhood information, and includes a list of neighboring APs, with the neighboring APs on the list being the ones with which the STA will try to associate should a handoff be required. Handoffs are under control of each STA, which makes its own independent judgment of the quality of signal that it receives, and whether a stronger (and therefore better) signal is available from another AP. To perform a handoff, the STA  
20 connects to the new AP in a different time-slot of the TDMA scheme, establishes communication, then releases the connection to the old AP, thereby performing a seamless transition from one AP to the next.

25 However, if the timing of APs is not synchronized, there is increased likelihood that a loss of connection could result. As a result, for example, in WMTS, when a patient is being transported from one wing of the hospital to another, their vital signs will no longer be monitored if the connection with a receiving station is lost.

Thus, there is a need in the art for a timing synchronization system that does not require additional complex circuitry or add large costs to the system.

30 The presently claimed invention provides accurate timing synchronization for a time-division multiplexed (TDMA) system using the existing cables of a LAN, particularly when used with the power cable arrangement in the IEEE 802.3AF standard.

The presently claimed invention provides a method of synchronization for multiple Access Points under 802.3 so that a handoff of a portable wireless device from one Access

Point to another can occur seamlessly. The method includes assignment of a specific number of pairs of a Category 5 (hereinafter "Cat 5") cable, so that a predetermined number of pairs are used to distribute power and synchronization information while the other two pairs are used to distribute data.

5           The present invention also provides a Wireless Medical Telemetry System (WMTS) Synchronization Unit (SU). The SU typically includes a block of RJ-45 connectors, with a first predetermined number of connectors that can be connected to a plurality of outputs of an Ethernet switch or hub, via a 'power over Ethernet unit' (which may be included in the switch or hub) conformant with the IEEE P802.3AF standard. A second predetermined  
10       number of connectors may be connected to a plurality of AP's and/or slave SUs, delivering Ethernet data, power and synchronization signals via the Cat 5 cable. In a network of SUs and APs, one SU will be designated the master WMTS Synchronization Unit, and will connect to a suitable 'master input' socket in each of a plurality of slave SUs. The slave SUs will in turn feed APs or further slave SUs.

15           The WMTS SU may include a socket intended for an external frequency standard, including but not limited to, a 10MHz reference standard. Hence, a given SU can either freely run, or be a slave to an external frequency standard, or may be slaved to a remote "Master".

          One advantage of the presently claimed invention is that if a Wireless Medical  
20       Telemetry System (WMTS) synchronization fails, power can still be provided to the APs through its circuit. In addition, the individual slave SUs can continue a free-run, synchronizing the islands of APs.

          Fig. 1 illustrates a system having two or more Access Points and a plurality of stations/device in communication, with a specific Access Point.

25           Fig. 2 illustrates a Category 5 cable having four twisted pair of wires.

          Fig. 3 illustrates two types of RJ-45 connectors, and provides a wire schematic according to the present invention.

          Fig.4A illustrates a diagrammatic view of the synchronization pulses at the AP/SU according to the present invention.

30           Fig. 4B illustrates a diagrammatic view of the synchronization pulses at the slave SU output.

          Fig. 5 illustrates a layout of a portion of a WMTS Synchronization Unit (SU) according to the present invention.

Fig. 6 illustrates a block diagram of a WMTS Synchronization Unit (SU) according to the present invention.

It is to be understood by persons of ordinary skill in the art that the following descriptions are provided for purposes of illustration and not for limitation. An artisan understands that there are many variations that lie within the spirit of the invention and the scope of the appended claims. Unnecessary detail of known functions and operations may be omitted from the current description so as not to obscure the finer points of the present invention.

The basic steps of a method according to the present invention can include the following:

- (a) arranging a cable comprising at least four pairs of twisted wires connected between an Ethernet LAN and each fixed Access Point (AP) and/or a nearby Synchronization Unit (SU) in a network, and between master and slave SUs;
- (b) assigning a first pair of the four pairs of twisted wire to carry a positive D.C. rail voltage to at least one AP or SU;
- (c) assigning a second pair of the four pairs of twisted wire to carry a negative D.C. rail voltage to the AP or SU;
- (d) providing to at least one of the first and second pairs of rails a series of synchronization pulses generated from a synchronization source and capacitively-coupled to a first end of the first and second pairs of twisted wire so as to supply a composite signal; and
- (e) reconstructing the generated synchronization pulses by detecting pulses on the positive and negative D.C. voltage rails at a second end by said at least one AP or SU.

While the following descriptions elaborate the steps recited above, it is to be stressed that the claimed invention is not limited to the details that have been provided in this application merely for illustrative purposes.

Fig.2 illustrates a Category 5 cable 201 (or "Cat-5"), which is a particular type of network cabling that comprises four twisted pair of copper wires 205, 207, 209, 211, normally color coded (each pair having one wire with a solid color such as orange, brown, green or blue). Cat-5 cables are capable of supporting frequencies up to 100 MHz and speeds of 1000Mbps, and are typically used for ATM, token ring, 1000Base-T, 100Base-T and 10Base-T networking. It should be understood that while Cat-5 cables are used here, any multiple twisted pair cable having at least four pairs of twisted wires and able to handle

minimum frequency requirement can be used to connect multiple stationary Access Points between each other.

In this application the RJ-45 connectors are wired for “straight through” connections as shown by connector 305 and wiring diagram 310 in Fig. 3. However, it is possible that a crossover configuration, as shown by wiring diagram 360, results in the wiring of connector 355.

It had been the custom that pins 4, 5, 7, 8 of the RJ-45 were unused. However, the IEEE 802.3AF standard adopted the use of pins 4 and 5 being connected together and carrying the positive rail of the 48V power supply, and pins 7 and 8 being connected together and carrying the negative rail of the 48V power supply. According to the present invention, power and synchronization information are sent over the same conductors within the network wiring. The 48 Volts is supplied by a power hub (which can either be separate or be included within an Ethernet hub or switch) which complies with the IEEE 802.3AF standard.

According to the present invention, the synchronization pulses are capacitively coupled to the power rails, which are isolated from the Power Hub by small inductors. Due to this coupling scheme, synchronization pulses are sent as alternative positive and negative differentiated pulses: the pulse receiver detects these pulses on the positive and negative rails of the 48V supply and uses voltage comparators and a D-type flip-flop to reconstruct the synchronization signal.

The synchronization pulses do not interfere with the correct operation of the voltage rails, particularly when it operates the IEEE 802.3AF scheme to detect the presence of a correct load on the end of the relevant cables prior to applying power. A power detection circuit on each input from the Power Hub ensures that the synchronization pulses are only sent after at least 36 volts is detected for at least 200ms, in order to prevent disturbance of the IEEE 802.3AF detection system.

Fig.4A illustrates a diagrammatic view of the synchronization pulses at the AP/SU. In normal operation, and as shown in Fig. 4A, three pulse widths can be generated wherein:

- (1) standard frame pulses 401 typically have a high duration of  $150\mu s \pm 50\mu s$ ;
- (2) multiframe pulses 403 that are not PSCN (Primary receiver Scan Carrier Number) synchronization pulses have a high duration of  $2ms \pm 50\mu s$ ;
- (3) multiframe pulses 405 that are PSCN synchronization pulses have a high duration of  $5ms \pm 50\mu s$ .

The synchronization signals can be sent pseudo-differentially, in a positive sense on pins 4, 5 (together), and in a negative sense on pins 7, 8. The pulse amplitude is 2V +/- 10% with a rise-time of 100ns +/- 25 as when the output is connected to a Master SU input or the input of an AP.

5 This circuit is galvanically isolated from the rest of the WMTS synchronization unit in accordance with the standard IEEE 802.3 section 14.3.1.1., to at least 2M ohm DC resistance and with a dielectric strength of 2.4kV. This isolation is also provided between interfaces, to ensure that no assumptions are made about the Power Hub itself (e.g. which line it switches, which rails it regards as common for sensing, etc.).

10 The synchronization pulses may be modified to suit different frame lengths and numbers of carriers in a WMTS or other communication system.

Alarm pulses (414 in Fig. 4A, 444 in Fig. 4B) can be sent if there is a problem with the synchronization pulses downstream.

Fig. 5 illustrates a layout of a portion of a WMTS Synchronization Unit (SU) according to the present invention. It is to be understood by persons of ordinary skill in the art that the illustrations presented herein are for purposes of illustration and not for limitation. If an artisan changes the shape, number of rows, positions, arrangement of the item, or details of the circuitry it still clearly falls within the spirit of the invention and the scope of appended claims.

20 First, it should be noted that the WMTS SU 500 does not require a microprocessor or software, as it preferably comprises a field-programmable gate array (FPGA) to accommodate at a low cost the divider chain and pulse generation and the ability to detect some fault conditions. The functional blocks that comprises the FPGA include input conditioning module 632, divider chain and delay compensation module 634, reference input select 637, LED driver 639 and frequency locked loop 645.

25 In this particular illustration, there are two blocks of RJ-45 connectors 505, 515 on the front panel (please note, it could also be a rear panel, side panel, etc.). The bottom 12 RJ-45 sockets (AP/SU 1 to 12) can be connected to APs and/or slave SUs. The signals emanating from these RJ-45 connectors are Ethernet, and when connected to an AP, or the 'Master SU' input of another SU, 48 volts plus synchronization signal. The upper 12 sockets 525, 530 connect to a power hub (or Ethernet switch or hub including the IEEE 802.3AF powering scheme) 555 which delivers power over the LAN according to the IEEE

802.AF scheme. An isolation barrier is provided between each of the twelve pairs of Ethernet sockets and the rest of the WMTS SU.

The SU also includes a further synchronization output 535, called a 'Slave SU' (this output is not applicable to an AP), designed to feed a slave SU only with WMTS synchronization pulses. This connects to the 'Master SU' socket 545 of the slave SU. A switch 550 sets the WMTS SU into one of master or slave mode. A adjustment unit 560 is included, which may comprise a switch or an equivalent function to adjust the compensation delay for the length of the Cat 5 cable between a remote master WMTS SU and the 'Master SU' input of the slave WMTS SU.

The WMTS SU can either freely run, or be a slave to an external 10 MHz source, or be a slave to a remote master. If the 10 MHz source and the remote Master SU source are both available, the selection is made automatically and the output of the SU is slaved to the latter. It is preferable but not required that the Master SU at the head of a chain in a large installation should be fed from an external 10MHz frequency standard, and that other SUs should operate in slave mode using the synchronization pulses from the Master SU.

As shown in Fig. 6, a power supply module 610 provides +5V, +12V and -12V rails. Linear sub-regulators 620 on the WMTS Synchronization Board provide +3.3V and +2.5V for the FPGA and other digital circuitry. The input power (line receiver 630) from the Master SU 545 comprises the +48V and the -48 Volts by pins 4, 5 and 7, 8, respectively. The external 10 MHz input signal receiving unit 635 is preferably a BNC or other coaxial connection. The time base VCXO 640 in this particular case uses a 20 MHz +/- 15 ppm temperature-compensated voltage-controlled crystal oscillator, but persons of ordinary skill in the art may substitute according to need and cost. The VCXO supplies a 20 MHz frequency locked-loop to minimize the disruptive effect of a break in the chain of the WMTS SUs, presuming there are several units chained together.

As shown in box 655 (the synchronization pulse injection unit), power and synchronization pulse information is sent over the same conductors within the network wiring. As the pins 4, 5 and 7, 8 (or the RJ-45 connectors) are connected together to a respective +48v rail and -48V rail are capacitively coupled via capacitors 656 to the power rails (high frequency pulses encounter little impedance from the capacitors), which in turn are isolated from the power hub by small inductors (as high frequency pulses encounter high impedance with the inductors).

Various modifications can be made to the present invention without departing from the spirit of the invention or the scope of the appended claims. For example, while cables such as Category 5 twisted wire and connectors such as RJ-45s are preferred, neither is a requirement to practice the claimed invention. The wires could be straight, or the category  
5 of cable could be different. The 48 volts rail voltage happens to be a standard, but the standard and/or the voltage could be changed according to need so that it could be any amount. The number of wire pairs in the cable can be greater or less than four, and while the RJ-45 connector is preferred, equivalent or successors of the RJ-45 connector that serves a similar purpose could be used. The external timing source can be a frequency less than or  
10 greater than 10 MHz, and depending on need could range anywhere from fractions to multiples of this 10 MHz value. Nor does the connection require a BNC connector. Functionally equivalent circuitry could be used in lieu of the items shown in Figs. 5 and 6. Also, the voltage value and frequency of the synchronization pulses shown in Figs. 4a and 4B are provided for illustrative purposes and can be longer or shorter in length and/or  
15 repetition frequency, and lower or higher in voltage.